

9/989242

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Class	Subclass

**ISSUE CLASSIFICATION**

PATENT NUMBER

## U.S. REISSUE Patent Application

<p>MT O.I.P.E.</p> <p>SCANNED <u>HR 113</u> Q.A. <u>46</u></p>	<p>O.G. PUBLICATION DATE</p> <p>04/30/02</p>	<p>REISSUE PATENT DATE</p>
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APPLICATION NO. 09/989242	CONT/PRIOR D F	CLASS 257	SUBCLASS 666	ART UNIT 1652 3815	EXAMINER J Clark
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## APPLICANTS

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Leadframe Semiconductor integrated circuit device using the same  
and method of and process for fabricating the two

**TITLE**

7637. 913

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>  <input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____.  <input type="checkbox"/> The terminal ____ months of this patent have been disclaimed.	<b>DRAWINGS</b> Sheets Drwg. Figs. Drwg. Print Fig.			<b>CLAIMS ALLOWED</b> Total Claims Print Claim for O.G.	
				<b>NOTICE OF ALLOWANCE MAILED</b>	
				<b>ISSUE FEE</b> Amount Due Date Paid	
<b>FINAL SPRE REVIEW</b> _____ (INITIALS)				<b>ISSUE BATCH NUMBER</b>	

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